STIC EIC 2100
Search Request Form

Today's Date: 5/17/2006

What date would you like to use to limit the search?

Priority Date: 12/6/2000 Other:

Name GREG BENGZON

AU 2144   Examiner # 80501

Room # 4C79   Phone 23944

Serial # 10/007807

Format for Search Results (Circle One):

PAPER   DISK   EMAIL

Where have you searched so far?

USP DWPI EPO JPO ACM IBM TDB

IEEE INSPEC SPI   Other _____

Is this a "Fast & Focused" Search Request? (Circle One) YES NO
A "Fast & Focused" Search is completed in 2-3 hours (maximum). The search must be on a very specific topic and meet certain criteria. The criteria are posted in EIC2100 and on the EIC2100 NPL Web Page at http://ptoweb/patents/stic/stic-tc2100.htm.

What is the topic, novelty, motivation, utility, or other specific details defining the desired focus of this search? Please include the concepts, synonyms, keywords, acronyms, definitions, strategies, and anything else that helps to describe the topic. Please attach a copy of the abstract, background, brief summary, pertinent claims and any citations of relevant art you have found.

Is this request for a BOARD of APPEALS case? (Circle One)   YES   NO

A NETWORK OF CONTROLLED TARGET DEVICES

- RECEIVING EXTERNAL (BROADCAST) EVENT TIME
- IDENTIFY SOURCE OF A STANDARD TIME
- IDENTIFY TARGET DEVICE
- PRESET TARGET DEVICE (RECORDING) TIME ACCDG. TO STANDARD TIME
- TRIGGER RECORDING

MATSUSHIYA   PG PUB 2002/99811

STIC Searcher _____ **Phone** 2-3520

Date picked up 5/17/06   **Date Completed** 5/17/06

XCOPY

39/3,K/11 (Item 11 from file: 2) <u>Links</u>

   Fulltext available through: <u>Institute of Electrical and Electronics Engineers</u>   <u>USPTO Full Text Retrieval Options</u>
INSPEC

Title: Design and evaluation of a generic software architecture for on-demand video servers

Author Chien-Liang Liu, J.; Du, D.H.C.; Shim, S.S.Y.; Jenwei Hsieh; MengJou Lin

Author Affiliation: Sch. of Electr. Eng. & Comput. Sci., Washington State Univ., Pullman, WA, USA

Abstract: ...evaluation of a generic software architecture for on-demand video servers. We describe different key **components** for **controlling** the **storage** and **network** devices within the server. The interactive collaborations between these software components are also illustrated. The experimental results **indicate** a very promising direction in exploring the right combinations of these software components. The server is thus able to increase the number of **concurrent** video accesses with the same hardware configuration. For instance, with the right combinations, the system achieved about 80% of the **storage** system bandwidth of four disks, about 70% of the **storage** system bandwidth of six disks, and generally reached the maximal achieved SCSI bandwidth when eight... ...currently under construction across a variety of hardware platforms, including SMP, DMP and clusters of **PCs** or workstations. The most advanced prototype server is based on an SGI shared-memory multiprocessor with a mass **storage** system consisting of RAID-3 disk arrays. With all the enabling/management schemes, we were...

Identifiers: ...**network** device control... ...**storage** device control... ...**concurrent** video accesses... ...**storage** system bandwidth... ...mass **storage** system
   1999

39/3,K/4 (Item 4 from file: 2) <u>Links</u>
   Fulltext available through: <u>USPTO Full Text Retrieval Options</u>
INSPEC

07649587   **INSPEC Abstract Number:** B2000-08-6210L-150, C2000-08-7210N-129
Title: Webcasting-the broadcasters' perspective
Author Kozamernik, F.
Journal: EBU Technical Review   no.282   p. 28 pp.
Publisher: Eur. Broadcasting Union ,
Publication Date: March 2000 **Country of Publication:** Switzerland
CODEN: ETEREG **ISSN:** 1019-6587
   Material Identity Number: P908-2000-001
Language: English
 Subfile: B C

Title: Webcasting-the broadcasters' perspective

Abstract: This article is based on the work carried out by the former EBU Webcasting Group. It provides an update on the extremely fast developments in the area of Webcasting that have occurred since the publication of the Group's document, "BPN 022-Practical Webcasting". It also outlines some of the opportunities and challenges provided by Webcasting and gives some indication of the future prospects. In particular, the article explores the impact of the Internet on the broadcasting sector. We are witnessing the process of convergence between the Internet and the emerging digital terrestrial and satellite broadcast systems. The convergence of the PC and digital broadcast terminals is bringing about the delivery of new services as part of the multi-channel offerings from digital radio and television broadcasters.

Descriptors: digital audio broadcasting; ... ...digital video broadcasting; ... ...direct broadcasting by satellite... ...Internet
Identifiers: EBU Webcasting Group... ...Internet; ... ...satellite broadcast systems... ...digital terrestrial broadcast systems... ...digital broadcast terminals... ...digital radio broadcasting; ... ...digital television broadcasting; ... ...World Wide Web;
 2000

39/3,K/34 (Item 34 from file: 2) <u>Links</u>
  Fulltext available through: <u>USPTO Full Text Retrieval Options</u>

Title: Techniques for multimedia synchronization in network file systems

Abstract: ...data is the presence of multiple media streams, whose display must proceed in a mutually synchronized manner. The design of techniques for synchronization of multimedia data at the time of storage, and retrieval from network file servers is the subject matter of this paper. The authors present algorithms by which a file server can create a relative time system and synchronize media units transmitted by different sources on a network to construct a multimedia object. These algorithms stay robust in the absence of global clocks, in the presence of transmission jitter and generation rate mismatches. The authors develop a feedback technique by which the file server can detect asynchronies in display devices during retrieval of multimedia objects, and even restore synchrony by deleting or duplicating media units destined for asynchronous destinations. They then present strategies by which the file server can actually predict the time in future when the asynchrony of a device is expected to exceed the permitted bound... ...action to nullify the asynchrony in advance. These algorithms can be generalized to heterogeneous multimedia networks in which there may be variations in sizes of media units generated, differences in network locations of sources and destinations, etc.
Descriptors: ...synchronisation
Identifiers: multimedia synchronization; ... ...network file systems... ...mutually synchronized manner... ...network file servers... ...transmission jitter
  1993

45/3,K/2 (Item 2 from file: 350) **Links**
Derwent WPIX

```
015186417      **Image available**
WPI Acc No: 2003-246950/200324
XRPX Acc No: N03-196231
```
  Wireless network e.g. CSMA network, determines
  time to transmit information to radio frequency access point, based on
  transmission time-ordered list generated from
  isochronous device identification data

```
Patent Assignee: SPECTRALINK CORP (SPEC-N)
Inventor: AMANN K R; HAMILTON M A
Number of Countries: 001  Number of Patents: 001
Patent Family:
```

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
US 6496499	B1	20021217	US 98113649	P	19981223	200324	B
			US 99454137	A	19991203		

```
Priority Applications (No Type Date): US 98113649 P 19981223; US 99454137 A
  19991203
Patent Details:
```

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 6496499	B1		17	H04B-007/212	Provisional application US 98113649

```
Abstract (Basic):
...       A microprocessor generates a transmission time-ordered
   list from isochronous device identification data
   received from a radio frequency access point (302) and
   accordingly determines time to transmit information to...
...       E.g. carrier sense multiple access (CSMA) network.
      ...

...The figure shows the block diagram of the wireless network.
```

```
...Title Terms: NETWORK;
Manual Codes (EPI/S-X): W01-A03B...

...W01-A06C4...

...W01-A06E1...
```

...W01-A06F1A

45/3,K/13 (Item 13 from file: 350) **Links**
Derwent WPIX

013111048   **Image available**
WPI Acc No: 2000-282919/200024
Related WPI Acc No: 2004-782855
XRPX Acc No: N00-212960
  Network that allows telephone and video to share
  computer network with non-real-time traffic uses several device
  adapters creating frame of time that may be synchronized in
  plurality of device adapters
Patent Assignee: PATH 1 NETWORK TECHNOLOGIES INC (PATH-N); PATH 1
  TECHNOLOGIES INC (PATH-N)
Inventor: CRUZ R L; FELLMAN R D; PALMER D A
Number of Countries: 089 Number of Patents: 009
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
WO 200011820	A1	20000302	WO 99US18984	A	19990818	200024	B
AU 9956816	A	20000314	AU 9956816	A	19990818	200031	
US 6215797	B1	20010410	US 98136706	A	19980819	200122	
US 20010002196	A1	20010531	US 98136706	A	19980819	200131	
			US 2001761207	A	20010116		
EP 1105988	A1	20010613	EP 99943786	A	19990818	200134	
			WO 99US18984	A	19990818		
US 6246702	B1	20010612	US 98136706	A	19980819	200135	
			US 98224577	A	19981231		
US 20010002195	A1	20010531	US 98136706	A	19980819	200137	
			US 98224577	A	19981231		
			US 2001764779	A	20010117		
US 6661804	B2	20031209	US 98136706	A	19980819	200381	
			US 98224577	A	19981231		
			US 2001764779	A	20010117		
US 6751231	B2	20040615	US 98136706	A	19980819	200439	
			US 2001761207	A	20010116		

Priority Applications (No Type Date): US 98224577 A 19981231; US 98136706 A
  19980819; US 2001761207 A 20010116; US 2001764779 A 20010117
Patent Details:
Patent No Kind Lan Pg   Main IPC   Filing Notes
WO 200011820 A1 E   67 H04J-003/06
    Designated States (National): AE AL AM AT AU AZ BA BB BG BR BY CA CH CN
    CR CU CZ DE DK DM EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP
    KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG
    SI SK SL TJ TM TR TT UA UG US UZ VN YU ZA ZW
    Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR
    IE IT KE LS LU MC MW NL OA PT SD SE SL SZ UG ZW
AU 9956816   A   Based on patent WO 200011820
US 6215797   B1   H04J-003/06
US 20010002196 A1   H04B-007/212 Cont of application US 98136706
                                     Cont of patent US 6215797
EP 1105988   A1 E   H04J-003/06   Based on patent WO 200011820

**Network that allows telephone and video to share
computer network with non-real-time traffic uses several device
adapters creating frame of time that may be synchronized in
plurality of device adapters**

Abstract (Basic):
...   A number of device adapters create a frame of **time**, which
    may be **synchronized** in the number of device adapters and
    repeating periodically. The frame includes a number of phases. Each of
    the device adapters has one of the phases **assigned** to it and
    transmits the packets **received** at the **device** interface to
    the **network** medium during the phase assigned to it. The number
    of phases includes a free-access...
...   The **network** (110) may include a broadcast portion (1),
    The latter is an environment in which packets **generated** by one
    station are transmitted to each of the stations on the
    network (i.e., packets are broadcast throughout the
    network). Accordingly, collisions would occur in the broadcast
    portion (1) if the device adapters (1000) of...

...not present to control the transmission of packets. The broadcast
    portion (1) may be an **Ethernet network** or another type of
    network generally operating in a broadcast environment...

...a) a device adapter for regulating traffic in a **network**
        (...

...b) a method for regulating traffic in **network** including
    devices for **generating** packets of data...

...repeater hub for connecting several real time devices and non-real-time
    devices into a **network**
        . . .

...In computer **networks** that allow real-time traffic such as
    telephone and video to share a computer **network** with
    non-real-time traffic to provide quality-of-service latency and
    bandwidth guarantees for...

...Capable of creating virtual isochronous channels within a CSMA/CD
   Ethernet network. Provides an arbitration mechanism to
   control access to the **network** for time-sensitive signals and to
   minimize or substantially eliminate collisions...

...The drawing is a schematic view of an exemplary **network** in
   accordance with the present invention...

...**network** (110
Title Terms: **NETWORK;**
...International Patent Class (Main): **H04L-012/28**
International Patent Class (Additional): **H04L-012/56**
Manual Codes (EPI/S-X): **T01-F02A1**...

...**T01-H07C5A**...

...**W01-A01C**...

...**W01-A03A1**...

...**W01-A03B**...
...**W01-A06F**...

...**W01-A06G2**

45/3,K/29 (Item 29 from file: 350) **Links**
Derwent WPIX

```
010189805     **Image available**
WPI Acc No: 1995-091059/199512
XRPX Acc No: N95-071974
```
  Widely separated clock synchronisation for simulcast
  paging network - transmits time-mark at precise instant,
  with paging station receivers measuring time interval between time-mark
  receipt and transmission
```
Patent Assignee: GLENAYRE ELECTRONICS INC (GLEN-N)
Inventor: FAWCETT G S; GLESSNER D W; WITSMAN M L; WITSAMAN M L
Number of Countries: 022  Number of Patents: 009
Patent Family:
Patent No      Kind   Date      Applicat No     Kind   Date      Week
WO 9505039     A1     19950216  WO 93US8016     A      19930827  199512  B
CN 1086647     A      19940511  CN 93117995     A      19930929  199529
US 5481258     A      19960102  US 93105436     A      19930811  199607
FI 9600575     A      19960208  WO 93US8016     A      19930827  199617
                                FI 96575        A      19960208
EP 713619      A1     19960529  EP 93921192     A      19930827  199626
                                WO 93US8016     A      19930827
US 5697051     A      19971209  US 93105436     A      19930811  199804
                                US 95549055     A      19951027
EP 1063798     A2     20001227  EP 93921192     A      19930827  200102
                                EP 2000116478   A      19930827
KR 298987      B      20011022  WO 93US8016     A      19930827  200236
                                KR 96700709     A      19960212
CN 1053305     C      20000607  CN 93117995     A      19930929  200468
```

```
Priority Applications (No Type Date): US 93105436 A 19930811; US 95549055 A
   19951027
Patent Details:
Patent No  Kind Lan Pg   Main IPC      Filing Notes
WO 9505039     A1     54 H04B-007/00
   Designated States (National): CA FI KR
   Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT LU MC NL
   PT SE
CN 1086647     A         H04B-007/26
US 5481258     A      22 G05B-023/02
FI 9600575     A         H04L-000/00
EP 713619      A1 E   54 H04B-007/00    Based on patent WO 9505039
   Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT LI LU MC
   NL PT SE
US 5697051     A      20 H04B-007/19    Cont of application US 93105436
                                        Cont of patent US 5481258
EP 1063798     A2 E      H04H-003/00    Div ex application EP 93921192
                                        Div ex patent EP 713619
   Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT LI LU MC
   NL PT SE
KR 298987      B         H04B-005/04    Previous Publ. patent KR 96704402
```

Widely separated clock synchronisation for simulcast paging network - ...

...transmits time-mark at precise instant, with paging station receivers measuring time interval between time-mark receipt and transmission

...Abstract (Basic): A clock **synchronisation** system **synchronises** plural paging stations (24), operating with a system **controller** (23) locked to external reference, e.g. GPS satellite. Each station contains a clock indicating...

...A central microcontroller monitors station times and system clock timing information, i.e. time-mark **transmission** and precise instant thereof...

...Subsequently, the system clock **transmits** the time-mark **transmission** instant, and each paging station measures the interval between time-mark receipt and its original **transmission** time. Knowledge of the time-mark **transmission** instant and the propagation delay to each station enables determn. and correction of any error...

...USE/ADVANTAGE - For clock **synchronisation** in distributed **transmission** e.g. paging system, enabling multiple clock co-ordination economically, with min. extra **components** in each **receiving** station...

...Abstract (Equivalent): A clock **synchronisation** system **synchronises** plural paging stations (24), operating with a system **controller** (23) locked to external reference, e.g. GPS satellite. Each station contains a clock indicating...

...A central microcontroller monitors station times and system clock timing information, i.e. time-mark **transmission** and precise instant thereof...

...Subsequently, the system clock **transmits** the time-mark **transmission** instant, and each paging station measures the interval between time-mark receipt and its original **transmission** time. Knowledge of the time-mark **transmission** instant and the propagation delay to each station enables determn. and correction of any error...

...USE/ADVANTAGE - For clock **synchronisation** in distributed **transmission** e.g. paging system, enabling multiple clock co-ordination economically, with min. extra **components** in each **receiving** station...

...a) a system **controller** including a system clock for maintaining a
system time, said system **controller** being operative to generate
paging data blocks, said paging data blocks containing pages to be
broadcast, each of said paging data blocks containing a
start time and timing information, said timing information being
derived from said system clock and comprised of a time mark and a time
mark **send** time, said time mark **send** time indicating the
time of **transmit** as indicated by said system clock of a
previously **transmitted** time mark; and...

...i) a link receiver for receiving said paging data blocks from said
system **controller;**
        (...

...ii) a **transmitter** for **broadcasting** said pages contained in
said paging data blocks; and...

...iii) a station **controller** including a paging station clock for
maintaining a station time, said station **controller** receiving
said paging data blocks from said system **controller** and
forwarding said pages contained in said paging data block to
said **transmitter** for **broadcast** when said **station**
time of said paging station clock equals said **start** time
contained in said paging data block
...Title Terms: **SYNCHRONISATION;**
...International Patent Class (Main): **H04L-000/00**
Manual Codes (EPI/S-X): **W01-A04...**